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Karen Vertz	(Depositor's name)
Karen Vertz	(Signature)
1-10-05	(D)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/684,948	10/14/2003	Craig T. Salling	TI-32535.I	5390

TITLE OF INVENTION: ELIMINATING SUBSTRATE NOISE BY AN ELECTRICALLY ISOLATED HIGH-VOLTAGE I/O TRANSISTOR

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	03/21/2005
EXAMINER	ART UNIT	CLASS-SUBCLASS			
POMPEY, RON EVERETT	2812	438-224000			

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Date 10 JAN 2005

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant Craig T. Salling, et al. Docket Number: TI-32535.1

Serial No.: 10/684,948 Art Unit: 2812

Filed: 10/14/03 Examiner: Ron E. Pompey

For: Eliminating Substrate Noise by an Electrically Isolated
High-Voltage I/O Transistor

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NAME OF INVENTOR(S): Craig T. Salling, et al.	
TITLE OF INVENTION: Eliminating Substrate Noise by an Electrically Isolated High-Voltage I/O Transistor	
TI FILE NO.: TI-32535.1	DEPOSIT ACCT. NO.: 20-0668
FAXED: 1-10-05 DUE: 3-21-05 ATTY/SECY: GCH/kv	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/684,948 Filing Date: 10/14/03	

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